Using MATLAB to develop 5G RF Front End components and their control algorithms

By: Sean Lynch Senior Staff Engineer QUALCOMM (UK) Limited

3rd October 2018



Qualcom



Qualcom

Fabless semiconductor company

In 3G/4G LTE modem

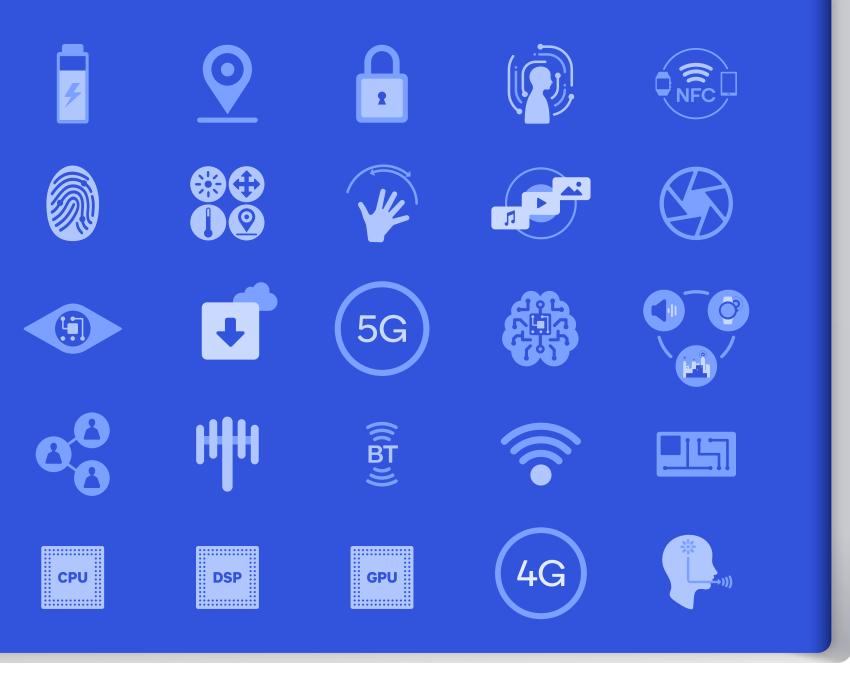
Ħ

30+

Years of driving the evolution of wireless

804M MSM[™] chipsets shipped FY '17

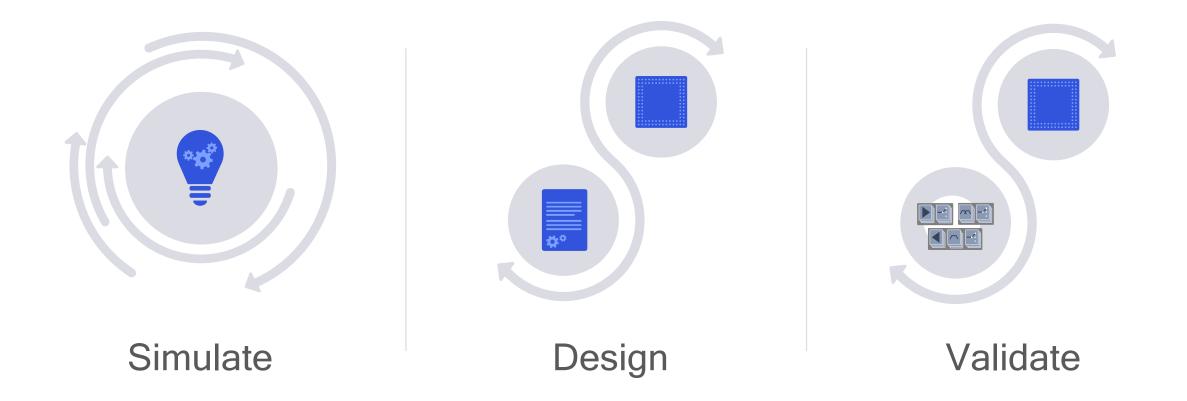
Sources: Qualcomm Incorporated data, as of Q4 FY17; IHS, May '18; MSM is a product of Qualcomm Technologies, Inc. and/or its subsidiaries



From the smartphone to 5G, it all starts with Qualcomm

\$53+ billion cumulative investment in R&D

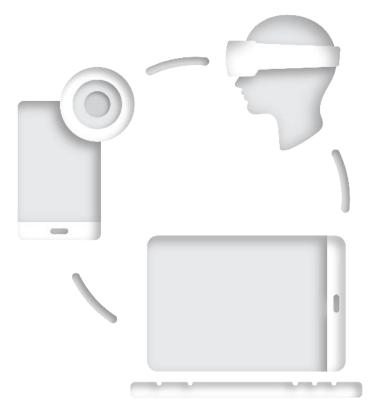
5G Development Process



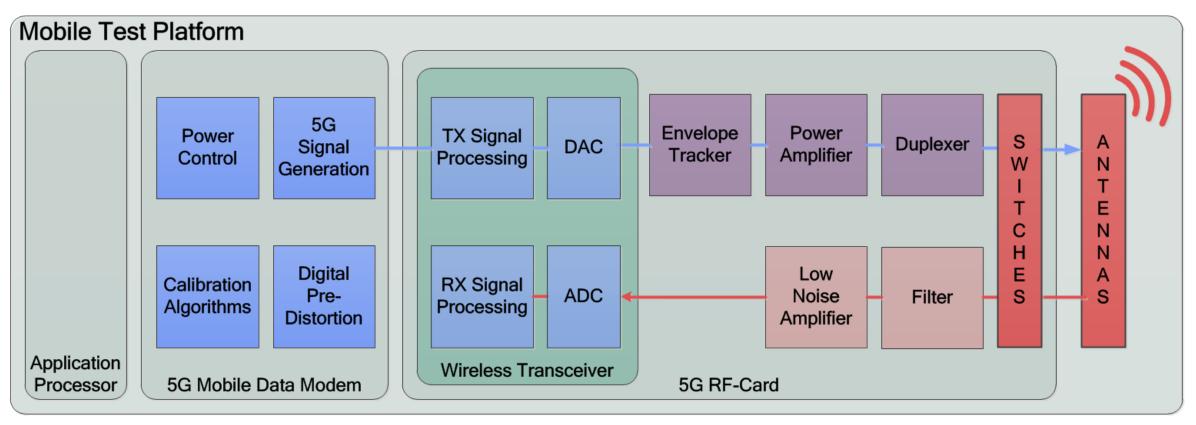
Build simulations that prove that the impossible is achievable Design the RF and Analogue hardware. Design the control algorithms Validate that the system meets the design requirements, at component, sub-system and phone levels.

5G Mobile

RF Front End - Simulation

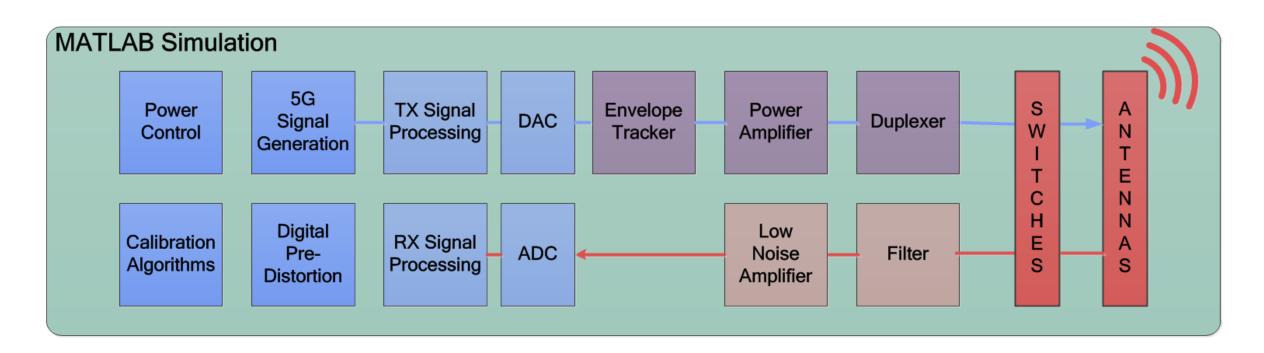


5G Target Hardware



The target hardware is a fully function phone including 5G Mobile Data Modem and 5G RF-Card The RF Front End supports over 30* different RF bands using multiple Power Amplifiers and Envelope Trackers

Simulation: Building the MATLAB System Model



MATLAB is used to build a complete model of the TX and RX paths

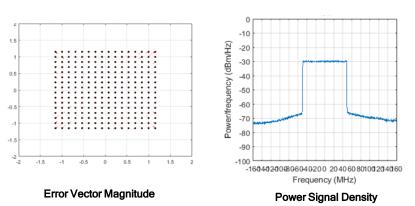
The digital blocks are modelled in a bit accurate manner We include accurate Power Amplifier models based on bench measurements

Toolbox's used: Signal Processing, DSP System, Communications System

Simulation: Predictions

Predicted System Parameters:

- Error Vector Magnitude
- Adjacent Carrier Leakage Ratio
- System Efficiency
- RX Band Noise



System Parameters that are optimized:

- PA Output Power
- Analogue Architectures
- Digital Settings
- DPD Settings

System Parameters that are swept:

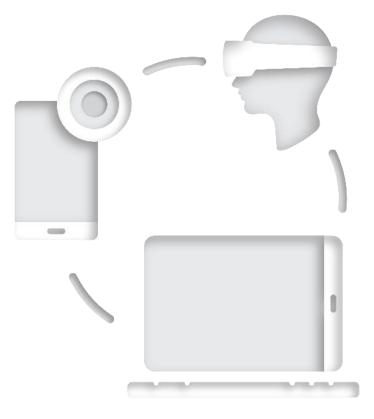
- Operating Band and Channel
- Channel Bandwidth
- Number of Resource Blocks
- Modulation Schemes
- Time Slot Allocation
- PA Output Power

At each test point the key system parameters can be predicted

Design parameters are optimized to balance performance against efficiency Predictions are repeated for different waveform types, to make sure we have a full solution

5G Mobile

RF Front End - Validation



Validation Challenges: Number of Waveform Combinations

5G NR (Sub 6MHz)		>10,000
4G (LTE-A 3 Component Carriers)	>1,000	
4G (LTE) >50		
Early 4G: 16		

Waveform Combinations by Technology [Not to scale]

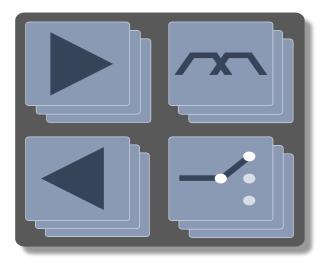
The number of possible Waveform Combinations is increasing exponentially with each new standard

Channel Bandwidths: Modulation Schemes: Active Resource Blocks: Time Slots per Sub-Frame: Frequency Division Multiplexer: Sub-Carrier Spacing: x10 x5 1 to 273 x8 (UL or DL) x2 x3 ¹⁰

Validation Challenges: Number of Supported RF Bands

Number of Supported RF Bands:

• 1990	2
• 2000	5*
• 2010	10*
• 2018	20*



PA module incl. duplexer (PAMiD)

Mobile phones are being used in more bands around the world.

Each band is powered by a different Power Amplifier chain.

LTE-A: 48 bands 5G NR: 26 bands - 5G is being targeted at some LTE bands

Validation Tools: Automated Test Sequencer

	Equipment		× 🗸 se									
	RF Test Equipment	Enable		м	odem M	lodel	ETTS	*				
	RFSIGGEN (rfTX)	TCPIP0::192.168.0.100::1			Definitior			OSW2820C\Ter	definitions)	QPM4620 Test se		dau.
	ARBSIGGEN (envArb)	TCPIP0::192.168.0.100::2		IBIL	Penniuor	n File	201014020	-Q3W2820C(Tes	definitions	QPM4020 Test se	quence vz.	disx .
	SIGAN (rfRX)	TCPIP0::192.168.0.120	~ R.	in Status								
	POWERMETER (paIn)	TCPIP0::192.168.0.140::1::I	com	pleted		test_n 31	um test_	tag Power Sweep - ca	dut_pa	dut_technology	dut_band B8	dut_rf_ IN1 TX
						32		Power Sweep - ca	QPM4620		B8	IN1 TX
	POWERMETER (paOut)	TCPIP0::192.168.0.140::2::I				33		Power Sweep - ca			B8	IN1_TX
						34		Power Sweep	QPM4620		B8	IN1_TX
	SCOPE (supplyIV)	TCPIP0::192.168.0.130::INS				35		Power Sweep - ca			B8	IN1_TX
	SIGAN (rxbn)					36		Power Sweep	QPM4620		B8	IN1_TX
						37	1RB	Power Sweep - ca	I QPM4620	LTE	B8	IN1_TX
	RFSIGGEN (rxDesense)		٠ _			_			_			_
	DUT		× Runni	ng test		-	out of	540	Passe	ed: -	Failed:	
		DUT-P9481-QET5100v1 -	Send	e-mail to						on error	🗌 on ca	mpletior
		QPM4620 -	✓ Te									
	QPOET	QET5100	Test C	ommen	t 🗌							
	Board ID	CAMBR.1.12		Operato	r 📃		SLYN	сн		🗌 Deb	ug Mode	
			Res	ults patł	· 📃	_	_		_		_	
	Calibration		× r ^{Mea}	suremen								
		uto Trace Loss				y _		🖌 Split Eff]	
						y _		✓ Split Eff ✓ Tracking				
	Trace	into Trace Loss Input Loss (dB)	× Meas									
	Trace	uto Trace Loss	× Mea:					✓ Tracking □ IM3, 5	g Analysis			
	Trace O	uto Trace Loss Input Loss (dB) 0.12 utput Loss (dB) 0.56						✓ Tracking IM3, 5 OoB En	g Analysis iissions			
	Trace O	into Trace Loss Input Loss (dB)						✓ Tracking IM3, 5 OoB En	g Analysis hissions s Emissions			
sl 23	Trace	uto Trace Loss Input Loss (dB) 0.12 utput Loss (dB) 0.56		System I ACLR Compre: Sample EVM				 Tracking IM3, 5 OoB Em Spuriou 	g Analysis hissions s Emissions			

test_num	d ut_technology	dut_band	d ut_rf_path	dut_chbw dat terting mode		dut_port	tx_frequency_mhz	waveform_from_file waveform_file name	waveform_gen_id	detrough_function	detrough_parameter detrough_filename	xpt_cal_vdd_min_v xpt_cal_vdd_max_v		v_dd_apt_v	psu_vbatt_v	v_su_vaux_v	psu_vcc1_v	power_cal_mode	I I	set_point_type ompression_point_db	pout_dbm pin_dbm	:lope_scale_backoff_db	de ky_mode	delay_method delay_manual_ns	delay_use_12rb	d pd_mode	dpd_method	dpd_use_12rb ad impedance ohm	etse_mode	eterologi, Lunkton eter, detrough, Lonkt Be, detrough, Jon, Junia Se, detrough, Juhh, vmax Se, detrough, Juhh, vmax Be, detrough, Juhh, vmax Be, detrough, Juhh, vmax detro data 1 data 1
-	-	-	-	.	-	-		-			- -		-	-	-			-			• •		¥	• •	T	-	-	T		
1	LTE B	12	IN1_TX7 :	5M E	T GO	7	707.5	0	1RB0	exponential			1 5.5	1.5	3.8 1	.8	с	alibration	vmax-cpoi	int 3		0 calibratio	n firmwa	re	0 cali	bration	bas	0	0	22
2	LTE B	12	IN1_TX7 5	5M E	T GO	7	707.5	0	1RB0	exponential			1 5.5	1.5	3.8 1	.8	n	euse	vmax-cpoi	int 3		0 reuse	firmwa	re	0 reu	se	bas	0	0	22
3	LTE B	12	IN1_TX7 S	5M E	T GO	7	707.5	0	1RB0	exponential			1 4.5	1.5	3.8 1	.8	С	alibration	vmax-cpoi	nt 3		0 calibratio	on firmwa	re	0 calil	bration	bas	0	0	22
4	LTE B	12	IN1_TX7 S	5M E	T GO	7	707.5	0	1RB0	exponential			1 4.5	1.5	3.8 1	.8	n	euse	vmax-cpoi	int 3		0 reuse	firmwa	re	0 reu:	se	bas	0	0	22
			IN1_TX7 S				707.5		1RB0	exponential					3.8 1		С	alibration	vmax-cpoi			0 calibratio				bration			0	22
			IN1_TX7 S				707.5		1RB0	exponential					3.8 1			euse	vmax-cpoi			0 reuse	firmwa		0 reu		bas		0	22
	LTE B		IN1_TX7 !				707.5		1RB0	exponential					3.8 1		С	alibration	vmax-cpoi			0 calibratio				bration			0	22
			IN1_TX7 S				707.5		1RB0	exponential					3.8 1			euse	vmax-cpoi			0 reuse	firmwa		0 reu:		bas		0	22
			IN1_TX7 S					0	1RB0	exponential					3.8 1				vmax-cpoi			0 calibratio				bration			0	22
			IN1_TX7 S				707.5	-	1RB0	exponential					3.8 1			euse	vmax-cpoi			0 reuse	firmwa		0 reu		bas	-	0	22
			IN1_TX7 :				707.5		12RB0	exponential					3.8 1				vmax-cpoi			0 calibratio				bration			0	22
	LTE B		IN1_TX7 :					0	12RB0	exponential					3.8 1			euse	vmax-cpoi			0 reuse	firmwa		0 reu		bas		0	22
	LTE B		IN1_TX7 :					0	12RB0	exponential					3.8 1				vmax-cpoi			0 calibratio				bration			0	22
	LTE B		IN1_TX7 :				707.5		12RB0	exponential					3.8 1			euse	vmax-cpoi			0 reuse	firmwa		0 reu		bas		0	22
			IN1_TX7 :				707.5			exponential					3.8 1				vmax-cpoi			0 calibratio				bration			0	22
	LTE B		IN1_TX7 :					0	12RB0	exponential					3.8 1			euse	vmax-cpoi			0 reuse	firmwa		0 reu		bas		0	22
	LTE B		IN1_TX7 : IN1 TX7 :				707.5 707.5	0		exponential exponential					3.8 1 3.8 1				vmax-cpoi vmax-cpoi			0 calibratio	firmwa		0 reu:	bration	bas		0	22 22
			IN1_1X7 :				707.5		12RB0	exponential					3.8 1			euse	vmax-cpoi vmax-cpoi			0 reuse 0 calibratio				se bration			0	22
			IN1 TX7 :				707.5		12RB0	exponential					3.8 1			euse	vmax-cpoi			0 reuse	firmwa		0 reu:		bas		0	22
	LTE B		IN1 TX7 :				707.5		50RB0	exponential					3.8 1				vmax-cpoi			0 calibratio				bration			0	22
	LTE B		IN1 TX7 :					0	50RB0	exponential					3.8 1			euse	vmax-cpoi			0 reuse	firmwa		0 reu		bas		0	22
	LTE B		IN1 TX7 :				707.5		50RB0	exponential					3.8 1				vmax-cpoi			0 calibratio				bration			0	22
	LTE B		IN1 TX7 :				707.5		50RB0	exponential					3.8 1			euse	vmax-cpoi			0 reuse	firmwa		0 reu:		bas		0	22
			IN1 TX7 :				707.5		50RB0	exponential					3.8 1				vmax-cpoi			0 calibratio				bration			0	22
	LTE B		IN1 TX7 :				707.5		50RB0	exponential					3.8 1			euse	vmax-cpoi			0 reuse	firmwa		0 reu		bas		0	22
	LTE B		IN1 TX7 :					0	50RB0	exponential					3.8 1				vmax-cpoi			0 calibratio	n firmwa	re		bration			0	22
	LTE B		IN1 TX7 :					0	50RB0	exponential					3.8 1			euse	vmax-cpoi			0 reuse	firmwa		0 reu		bas		0	22
			IN1 TX7 :			7	707.5	0	50RB0	exponential					3.8 1				vmax-cpoi			0 calibratio	n firmwa	re	0 cali	bration			0	22
	LTE B		IN1 TX7 :			7	707.5	0	50RB0	exponential					3.8 1			euse	vmax-cpoi			0 reuse	firmwa	re	0 reu:	se	bas	0	0	22
31	LTE B	12	IN1_TX7 S	5M E	T GO	7	701.5	0	1RB0	exponential			1 5.5	1.5	3.8 1	.8	с	alibration	vmax-cpoi	int 3		0 calibratio	n firmwa	re	0 cali	bration	bas	0	0	22
32	LTE B	12	IN1 TX7 5	5M E	T GO	7	701.5	0	1RB0	exponential			1 5.5	1.5	3.8 1	.8	n	euse	vmax-cpoi	int 3		0 reuse	firmwa	re	0 reu	se	bas	0	0	22
33	LTE B	12	IN1_TX7 :	5M E	T GO	7	701.5	0	1RB0	exponential			1 4.5	1.5	3.8 1	.8	с	alibration	vmax-cpoi	nt 3		0 calibratio	n firmwa	re	0 cali	bration	bas	0	0	22
34	LTE B	12	IN1_TX7	5M E	T GO	7	701.5	0	1RB0	exponential			1 4.5	1.5	3.8 1	.8	n	euse	vmax-cpoi	int 3		0 reuse	firmwa	re	0 reu	se .	bas	0	0	22
35	LTE B	12	IN1_TX7 S	5M E	T GO	7	701.5	0	1RB0	exponential			1 3.5	1.5	3.8 1	.8	с	alibration	vmax-cpoi	int 3		0 calibratio	n firmwa	re	0 cali	bration	bas	0	0	22
36	LTE B	12	IN1_TX7 3	5M E	T GO	7	701.5	0	1RB0	exponential			1 3.5	1.5	3.8 1	.8	n	euse	vmax-cpoi	int 3		0 reuse	firmwa	re	0 reu:	se	bas	0	0	22
37	LTE B	12	IN1_TX7 S	5M E	T GO	7	701.5	0	1RB0	exponential			1 3	1.5	3.8 1	.8	с	alibration	vmax-cpoi	nt 3		0 calibratio	on firmwa	re	0 cali	bration	bas	0	0	22
38	LTE B	12	IN1_TX7 S	5M E	T GO	7	701.5	0	1RB0	exponential			1 3	1.5	3.8 1	.8	n	euse	vmax-cpoi	int 3		0 reuse	firmwa	re	0 reu	se	bas	0	0	22
39	LTE B	12	IN1_TX7 5	5M E	T GO	7	701.5	0	1RB0	exponential			1 2.5	1.5	3.8 1	.8	с	alibration	vmax-cpoi	int 3		0 calibratio	on firmwa	re	0 cali	bration	bas	0	0	22

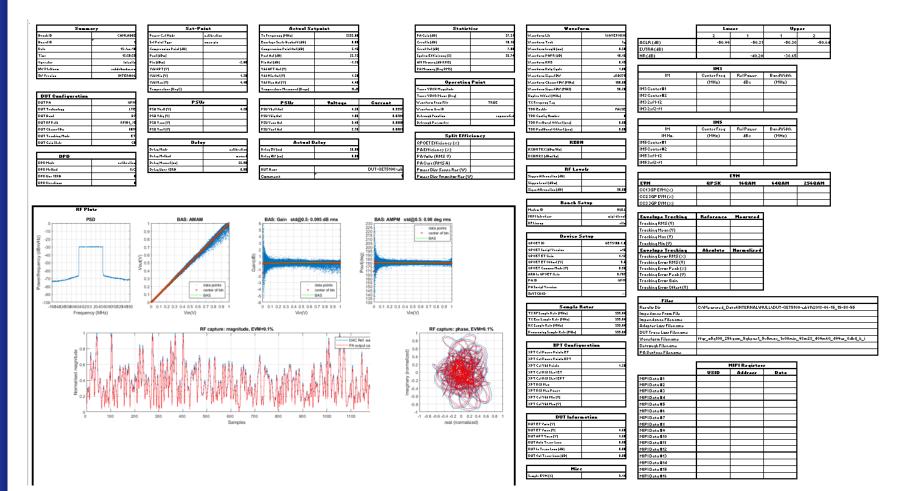
A test sequencer controls the DUT and test equipment. The same application is used to control all platforms. Test definitions are created using Excel files containing 2,000+ tests per Power Amplifier module We have over 50 test parameters that can be swept during a test sweep. From waveform to temperature.

Validation Tools: Data Visualization

Validation Tools

We record test parameters and RF measurement results using Excel

This is useful for smaller test runs, but parsing 1,000 Excel sheets is tedious for longer runs.



Detailed Measurement Report

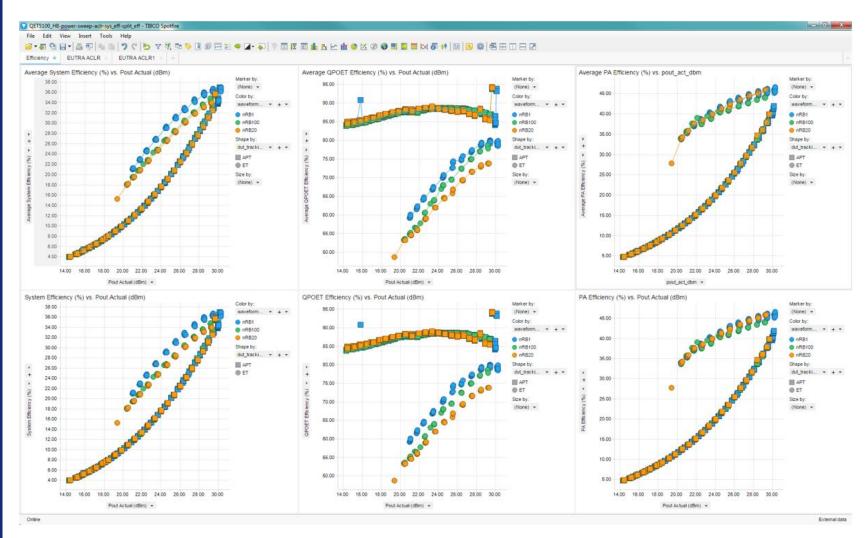
Validation Tools: Data Visualization

Validation Tools

We also store test parameters and detailed results in a MySQL database.

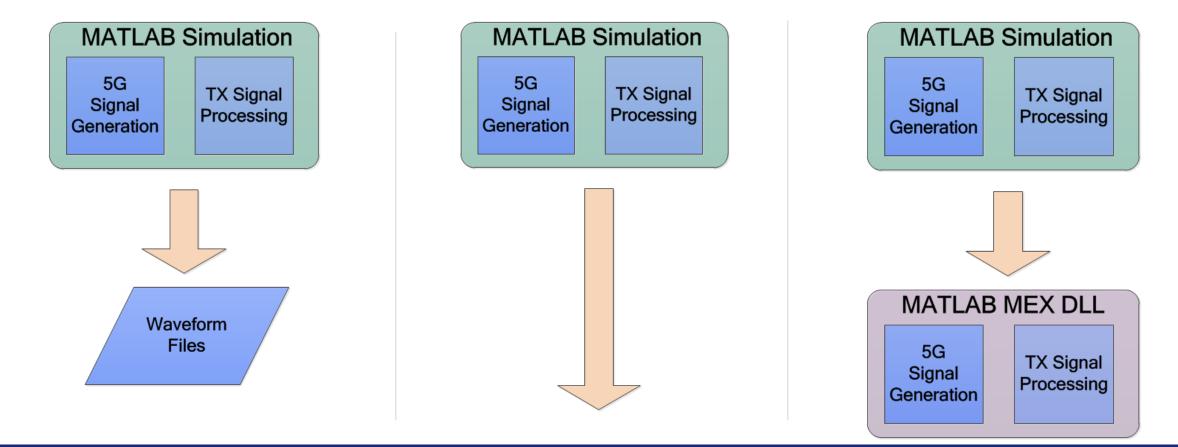
Detailed reports are extracted using a 3rd party report generator.

This approach makes comparing device models and variants a lot easier.



Database Extracted Report

Validation Tools: Waveform Generation



Phase#1 Generated waveform files using the MATLAB system simulation. At 250 LTE waveforms the release image was too large for our release system. Phase#2 Integrated simulation into the test software. Could not be shipped to customers as the IP was too sensitive. Phase#3 Used MATLAB Coder to create a MEX DLL, which allowed the sensitive IP to be released externally.

Waveform Generation: MATLAB CODER Challenges

- Code was not designed for code generation
 Needed to rewrite some sections that used cells (MATLAB R2017b and later supports cells)
- Large number of files involved; over 400 files per Modem model
- The code generation process is complicated having multiple phases:
 Review, Compile, Execute
- Specific versions of MATLAB need to be used, due to MEX API changes
- Needed to pass open source code scanning before external release.

Our first pass at generating a waveform library took several months

We can only ship binaries where the source code has been scanned for 3rd party code.

Waveform Generation: MATLAB CODER Advantages

- Building a library has the following advantages:
 - Dependences on MATLAB toolboxes are reduced
 - But this may affect performance (FFT*)
 - Replaces 400+ files with a single file:
 - Allows different waveform generation libraries to be used in parallel in an application
 - Enables secure IP delivery, both internally and externally.
 - Using waveform genration libraries removes the need to ship waveforms
 - Reduces build copy times from 2hrs to 15mins

Building a MEX library enables native MATLAB functions to be called.

Building a Windows DLL enables other technologies to use the IP block

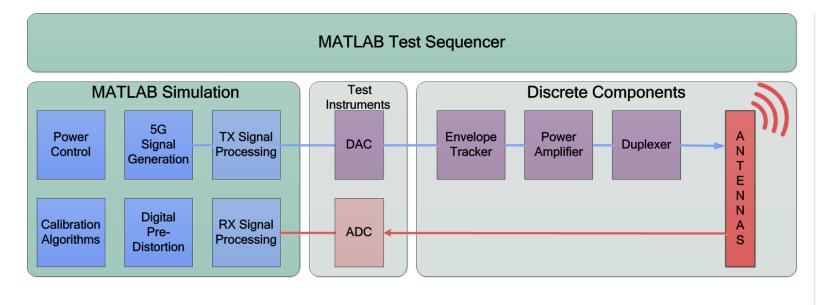
Waveform Generation: MATLAB CODER Successes

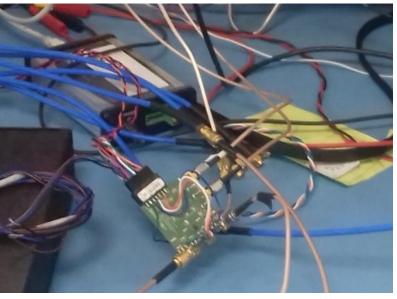
- We have now generated Modem waveform libraries for the following 3GPP standards:
 - LTE
 - LTE Advanced
 - LTE TDD Advanced
 - 5G NR

These libraries are fully integrated into our range of RF test applications. Building the 5G NR library took a few weeks, and can be rebuilt in hours as the standard matures.

We have also released waveform generation tools for IC simulation and Product Test departments.

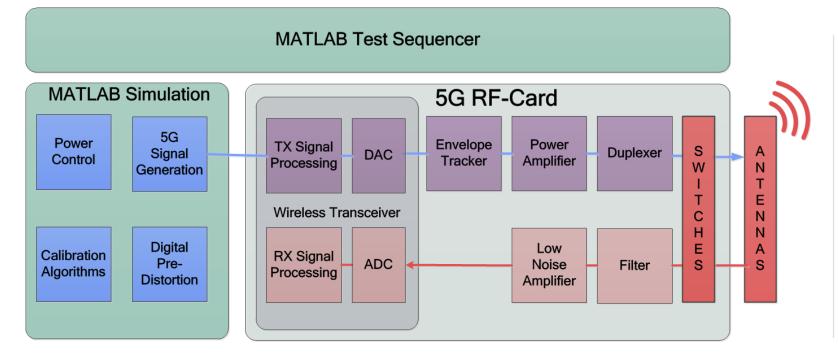
Validation Tools: Discrete Component Testing





Most of the MATLAB System Model is used to calibrate the system and generate pre-distorted test signals Third party test equipment is used to generate and capture the 5G RF signals. The PA and RF Systems groups use this system to validate components and algorithms

Validation Tools: RF-Card Testing with Wireless Transceiver





The MATLAB System Model is used to calibrate the system and generate pre-distorted test signals Settings from the MATLAB System model are used to program the Wireless Transceiver The IPS group uses this platform to validate RF-Cards. The systems group use it for algorithm development 20

Validation Challenges: Multiple Test Platform Support

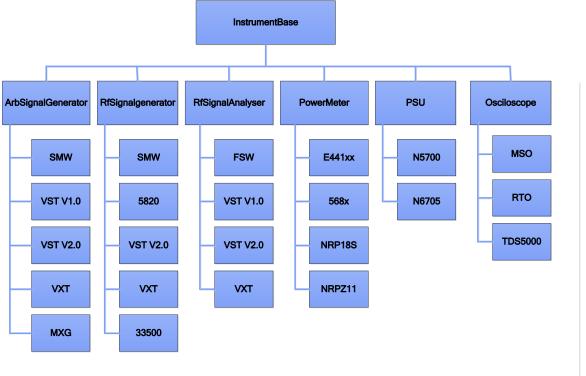


VST2.0

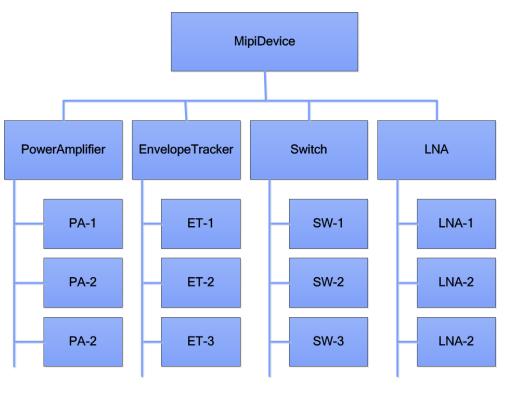


National Instruments: RF TX and RF TX are integrated into one card. Rohde & Schwarz: RF TX and Envelope TX are in the SMW. RF RX is in the FSW.

Scalability: Using MATLAB OO



Instrument Class Diagram



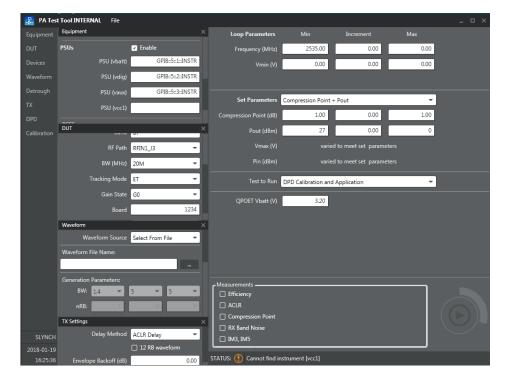
MIPI Device Class Diagram

We use abstraction to support many different types of test equipment, and DUT variants.

DUTs may contain any combination of: Envelope Tracker, Power Amplifier, RF Switches, Low Noise Amplifiers

Validation Challenges: Code Testing

User Interface Testing



Device Driver Testing



One GUI has over 50 different operating modes. Using MATLAB OO we are able to programmatically control and verify correct GUI operation. Using a class based on matlab.mixin.SetGet we can replace the Instrument Control VISA object. Thus allowing unit testing of instrument drivers

Summary

Qualcomm 5G built using MATLAB

- MATLAB has enabled. Qualcomm Technologies, Inc. to fully model the RF Transceiver and key analogue and RF components.
- The MATLAB models are then used to optimize and verify the RF Front End through all phases of its development.
- The MATLAB Coder product has allowed us to release sensitive IP both internally and externally in a secure manner.
- MATLAB's OO features have enabled a scalable and maintainable set of tests solution to be created by a small team.

Qualconn

Thank you!

Follow us on: **f** 🎔 in

For more information, visit us at: www.qualcomm.com & www.qualcomm.com/blog

Nothing in these materials is an offer to sell any of the components or devices referenced herein.

©2018 Qualcomm Technologies, Inc. and/or its affiliated companies. All Rights Reserved.

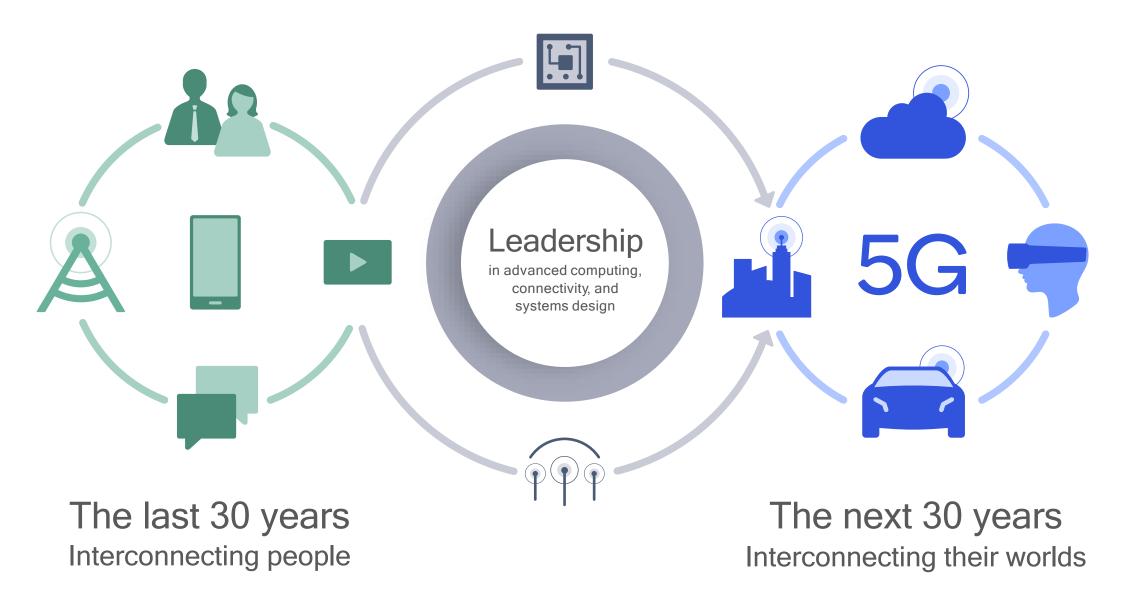
Qualcomm, MSM and Snapdragon are trademarks of Qualcomm Incorporated, registered in the United States and other countries. Other products and brand names may be trademarks or registered trademarks of their respective owners. References in this presentation to "Qualcomm" may mean Qualcomm Incorporated, Qualcomm Technologies, Inc., and/or other subsidiaries or business units within the Qualcomm corporate structure, as applicable. Qualcomm Incorporated includes Qualcomm's licensing business, QTL, and the vast majority of its patent portfolio. Qualcomm Technologies, Inc., a wholly-owned subsidiary of Qualcomm Incorporated, operates, along with its subsidiaries, substantially all of Qualcomm's engineering, research and development functions, and substantially all of its product and services businesses, including its semiconductor business, QCT.

Inventing the 5G foundation



Invention for what's next

in the increasingly smart and connected world



Our vision for 5G is a unifying connectivity fabric

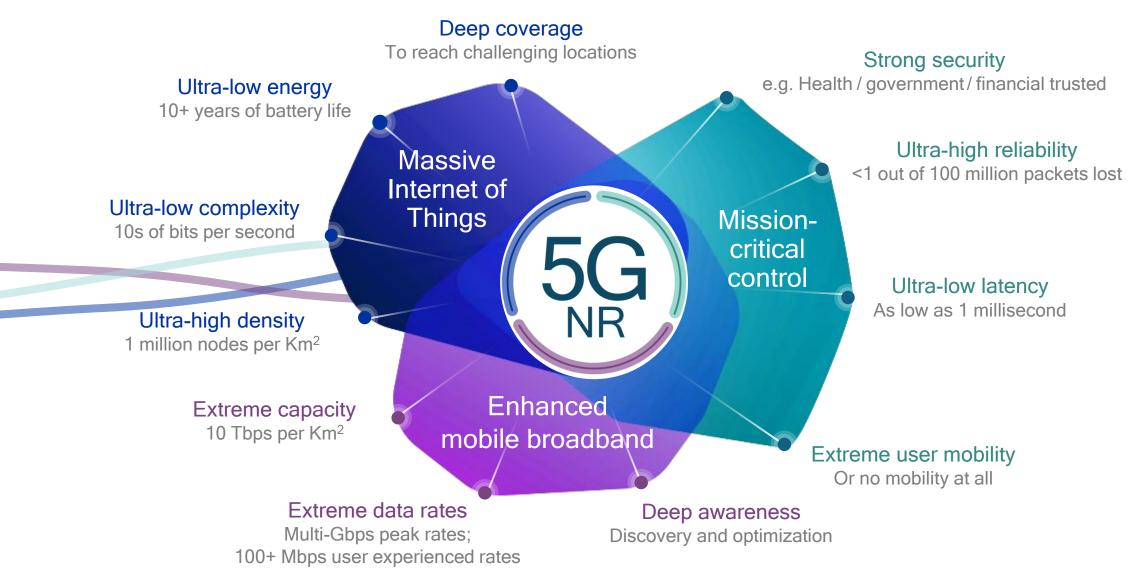
• Delivering always-available, secure cloud access

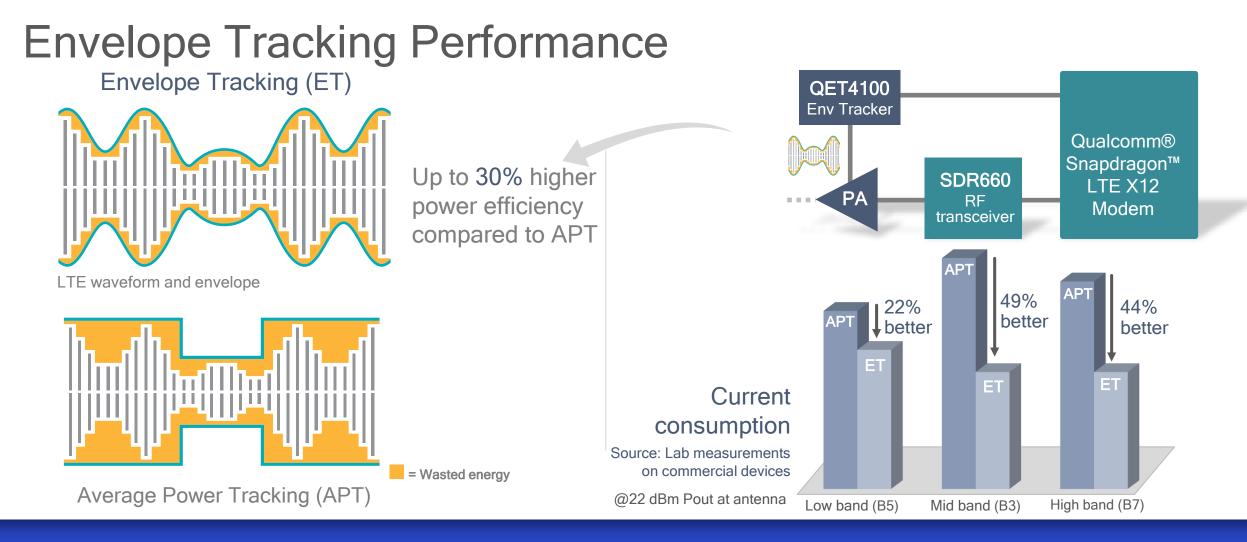


Unifying connectivity platform for future innovation

Convergence of spectrum types/bands, diverse services, and deployments

Scalability to address diverse service and devices





Envelope Tracking eliminates wasted PA power for LTE and 5G waveforms

Current consumption and thus battery life is a key performance indicators, especially for high end devices